

L Number	Hits	Search Text	DB	Time stamp
1	3084	PARALLEL WITH SORT\$3	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/24 13:15
2	171	((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED))	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/24 13:16
3	146	((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/24 13:16
4	22	((((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608) AND SPLIT	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/24 13:16
5	26	((((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608) AND SPLIT\$4	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/24 13:17
6	2	((((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608) AND "DECISION TREE"	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/24 13:21
7	5	((((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608) AND "DATA MINING"	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/24 13:32
9	0	((((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608) AND "DECISION TREE") AND ((((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608) AND "DATA MINING") AND ((((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608) AND SPLIT\$4) AND TREE)	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/24 13:32
10	1	((((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608) AND "DATA MINING") AND ((((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608) AND SPLIT\$4) AND TREE)	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/24 13:32
8	21	((((PARALLEL WITH SORT\$3) AND (OBJECT WITH ORIENTED)) AND @AD<20010608) AND SPLIT\$4) AND TREE	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/24 13:33

	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	3084	PARALLEL WITH SORT\$3	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/2 4 13:15			0
2	BRS	L2	171	1 AND (OBJECT WITH ORIENTED)	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/2 4 13:16			0
3	BRS	L3	146	2 AND @AD<20010608	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/2 4 13:16			0
4	BRS	L4	22	3 AND SPLIT	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/2 4 13:16			0

	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Errors
5	BRS	L5	26	3 AND SPLIT\$4	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/2 4 13:17			0
6	BRS	L6	2	3 AND "DECISION TREE"	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/2 4 13:21			0
7	BRS	L7	5	3 AND "DATA MINING"	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/2 4 13:32			0
8	BRS	L9	0	6 AND 7 AND 8	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/2 4 13:32			0

	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Errors
9	BRS	L10	1	7 AND 8	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/2 4 13:32			0
10	BRS	L8	21	5 AND TREE	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/2 4 13:33			0

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030105620 A1	20030605	320	System, method and article of manufacture for interface constructs in a programming language capable of programming hardware architectures	703/22		
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030074177 A1	20030417	314	System, method and article of manufacture for a simulator plug-in for co-simulation purposes	703/22		
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030046671 A1	20030306	323	System, method and article of manufacture for signal constructs in a programming language capable of programming hardware architectures	717/141		
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030046668 A1	20030306	323	System, method and article of manufacture for distributing IP cores	717/131		
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030037321 A1	20030220	327	System, method and article of manufacture for extensions in a programming language capable of programming hardware architectures	717/149		
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030033594 A1	20030213	324	System, method and article of manufacture for parameterized expression libraries	717/141	717/163	
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030033588 A1	20030213	321	System, method and article of manufacture for using a library map to create and maintain IP cores effectively	717/107	717/163	

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	Bowen, Matt	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20030105620	<input type="checkbox"/>
2	Bowen, Matt	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20030074177	<input type="checkbox"/>
3	Bowen, Matt	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20030046671	<input type="checkbox"/>
4	Bowen, Matt	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20030046668	<input type="checkbox"/>
5	Bowen, Matt	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20030037321	<input type="checkbox"/>
6	Bowen, Matt	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20030033594	<input type="checkbox"/>
7	Alexander, John	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20030033588	<input type="checkbox"/>

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030028864 A1	20030206	321	System, method and article of manufacture for successive compilations using incomplete parameters	717/141		
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020199173 A1	20021226	332	System, method and article of manufacture for a debugger capable of operating across multiple threads and lock domains	717/129	717/131	
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6691301 B2	20040210	270	System, method and article of manufacture for signal constructs in a programming language capable of programming hardware architectures	717/114	712/15; 716/16	
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6311265 B1	20011030	110	Apparatuses and methods for programming parallel computers	712/203		
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6292822 B1	20010918	37	Dynamic load balancing among processors in a parallel computer	718/105	718/102; 718/103	
13	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6289334 B1	20010911	98	Apparatus and method for decomposing database queries for database management system including multiprocessor digital data processing system	707/3	707/10; 707/2; 707/201; 709/201; 709/208	
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6241073 B1	20010605	10	Wood aligning and orienting conveyor	198/382		
15	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6212617 B1	20010403	34	Parallel processing method and system using a lazy parallel data type to reduce inter-processor communication	712/3	345/505; 712/4	

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
8	Bowen, Matt	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20030028864	<input type="checkbox"/>
9	Bowen, Matt	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20020199173	<input type="checkbox"/>
10	Bowen, Matt	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6691301	<input type="checkbox"/>
11	Beckerle, Michael J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6311265	<input type="checkbox"/>
12	Hardwick, Jonathan C.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6292822	<input type="checkbox"/>
13	Reiner, David et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6289334	<input type="checkbox"/>
14	McAllister, Barry James et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6241073	<input type="checkbox"/>
15	Hardwick, Jonathan C.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6212617	<input type="checkbox"/>

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif
16	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6106575 A	20000822	34	Nested parallel language preprocessor for converting parallel language programs into sequential code	717/119	712/229; 712/28; 717/149; 717/162; 718/102; 718/105; 718/108; 719/331	
17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6088511 A	20000711	40	Nested parallel 2D Delaunay triangulation method	717/149	345/423; 712/36	
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6028608 A	20000222	66	System and method of perception-based image generation and encoding	345/619		
19	<input type="checkbox"/>	<input type="checkbox"/>	US 5987468 A	19991116	38	Structure and method for efficient parallel high-dimensional similarity join	707/100	707/6; 707/7; 712/11	
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5867649 A	19990202	109	Dance/multitude concurrent computation	709/201		
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5742806 A	19980421	304	Apparatus and method for decomposing database queries for database management system including multiprocessor digital data processing system	707/3	710/20; 711/150; 711/216; 714/11	

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	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
16	Hardwick, Jonathan C.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6106575	<input type="checkbox"/>
17	Hardwick, Jonathan C.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6088511	<input type="checkbox"/>
18	Jenkins, Barry	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6028608	<input type="checkbox"/>
19	Singh, Vineet et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5987468	<input type="checkbox"/>
20	Larson, Brian Ralph	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5867649	<input type="checkbox"/>
21	Reiner, David et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5742806	<input type="checkbox"/>

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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6687695 B1	20040203	11	SQL-based analytic algorithms	707/6	707/101; 707/102; 707/5	
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6611829 B1	20030826	14	SQL-based analytic algorithm for association	707/2	707/10; 707/3; 707/6; 715/509	
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6553366 B1	20030422	16	Analytic logical data model	707/2	707/100; 707/101; 707/104.1; 707/3; 707/4; 707/6	D50
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5987468 A	19991116	38	Structure and method for efficient parallel high-dimensional similarity join	707/100	707/6; 707/7; 712/11	11 D97 Tree
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5802357 A	19980901	47	System and method for performing an efficient join operation	707/2		11 merge

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	Miller, Timothy Edward et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6687695	<input type="checkbox"/>
2	Tate, Brian Don et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6611829	<input type="checkbox"/>
3	Miller, Timothy Edward et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6553366	<input type="checkbox"/>
4	Singh, Vineet et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5987468	<input type="checkbox"/>
5	Li, Zhe et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5802357	<input type="checkbox"/>

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif
①	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6311265 B1	20011030	110	Apparatuses and methods for programming parallel computers	712/203		
②	<input type="checkbox"/>	<input type="checkbox"/>	US 5805915 A	19980908	30	SIMIMD array processing system	712/20	712/16; 712/22	SW, RES

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	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	Beckerle, Michael J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6311265	<input type="checkbox"/>
2	Wilkinson, Paul Amba et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5805915	<input type="checkbox"/>